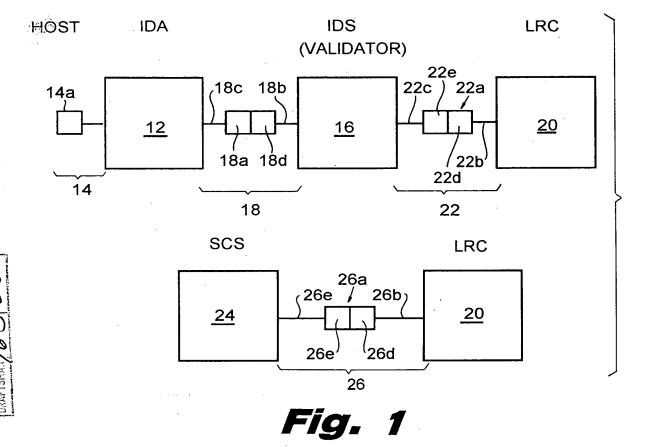
APPESOVED O.G. FIG

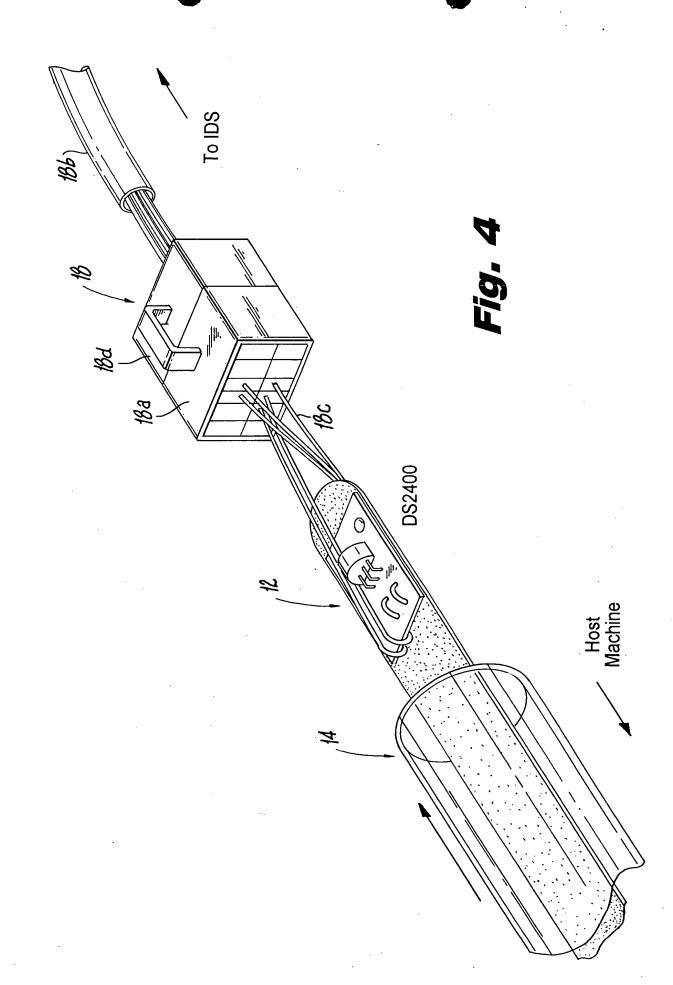


210 5V 216 8052 CPU 5.6k 214 212 p 1.0 to DS2250 5V 222 5.6K 220 218 p.3.6 to DS2400 16 Fig. 2

RESET = 0CARRY **RET** CC SEND SKIP ROM SEND SKIP ROM CC SEND WRITE SCRATCH PAD SEND COPY SCRATCH PAD **5**J OF **FROM** SEND LOWER ADDRESS **SEND LOWER ADDRESS** IN RO **BUFF** FROM IN R1 SEND UPPER ADDRESS SEND UPPER ADDRESS **BUFF+1 FROM** SEND DATA BYTE SEND E/S BYTE IN R7 BUFF+2 RESET **RESET** CARRY **RET RET** SEND SKIP ROM CC SEND READ SCRATCH PAD AA **READ LOWER ADDRESS** IN BUFF **READ UPPER ADDRESS** IN BUFF+1 **READ E/S BYTE** IN BUFF+2 YES. ERROR **READ DATA BYTE** IN R7 **RESET** NO CARRY RET

eres 0.G. F1G. BY CLASS SUBCLASS FISHAR

Fig. 3



APPEATED O.G. F.IG.

DRAFTSKAN

APPLEADE (1.G. FIG. BY CLASS SUBCLASS